

MS1-796US 1 of 6

Inventor: Dean

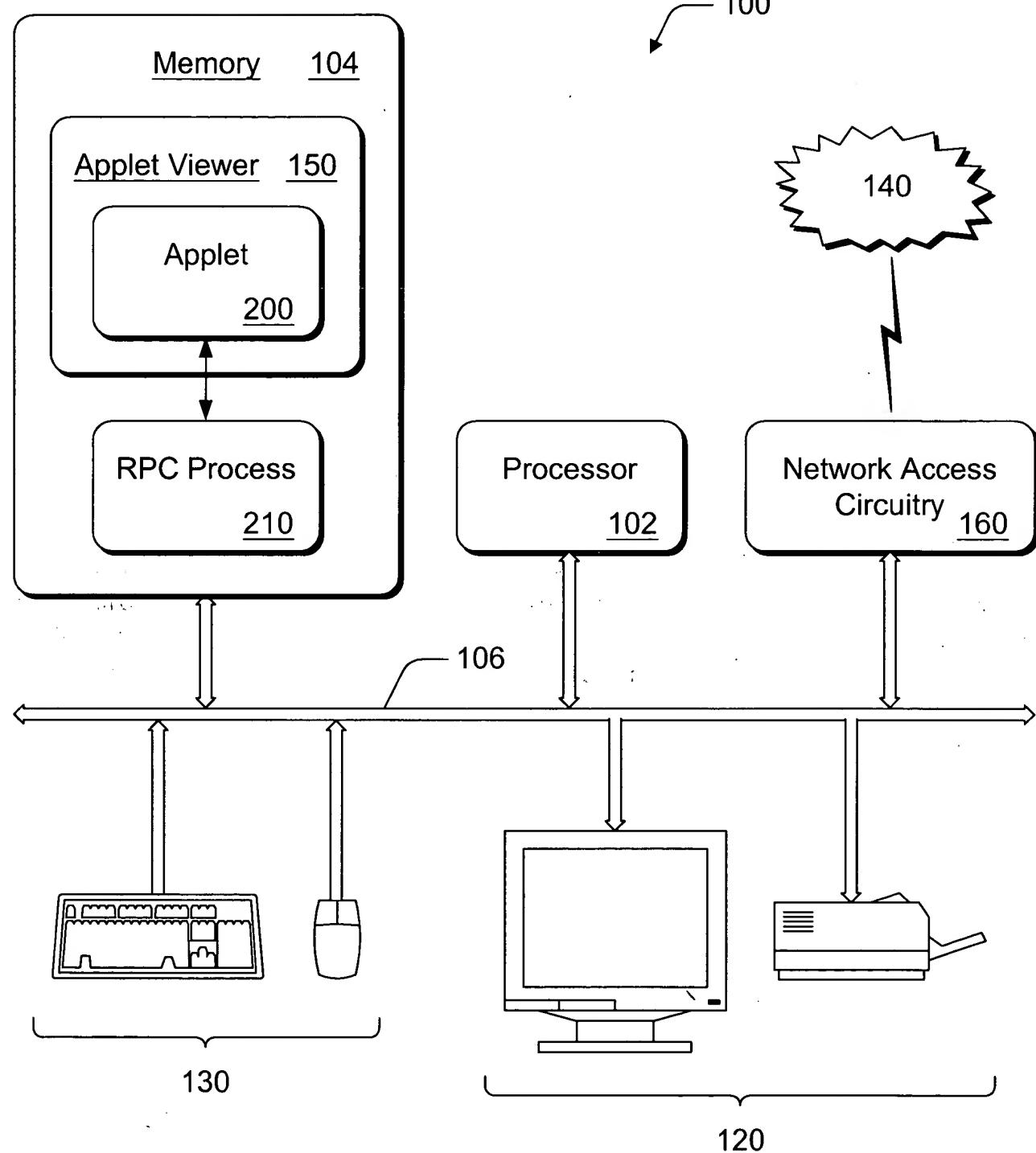
Title: Interprocess Communication Mechanism
for Heterogeneous Computer Processes

Fig. 1

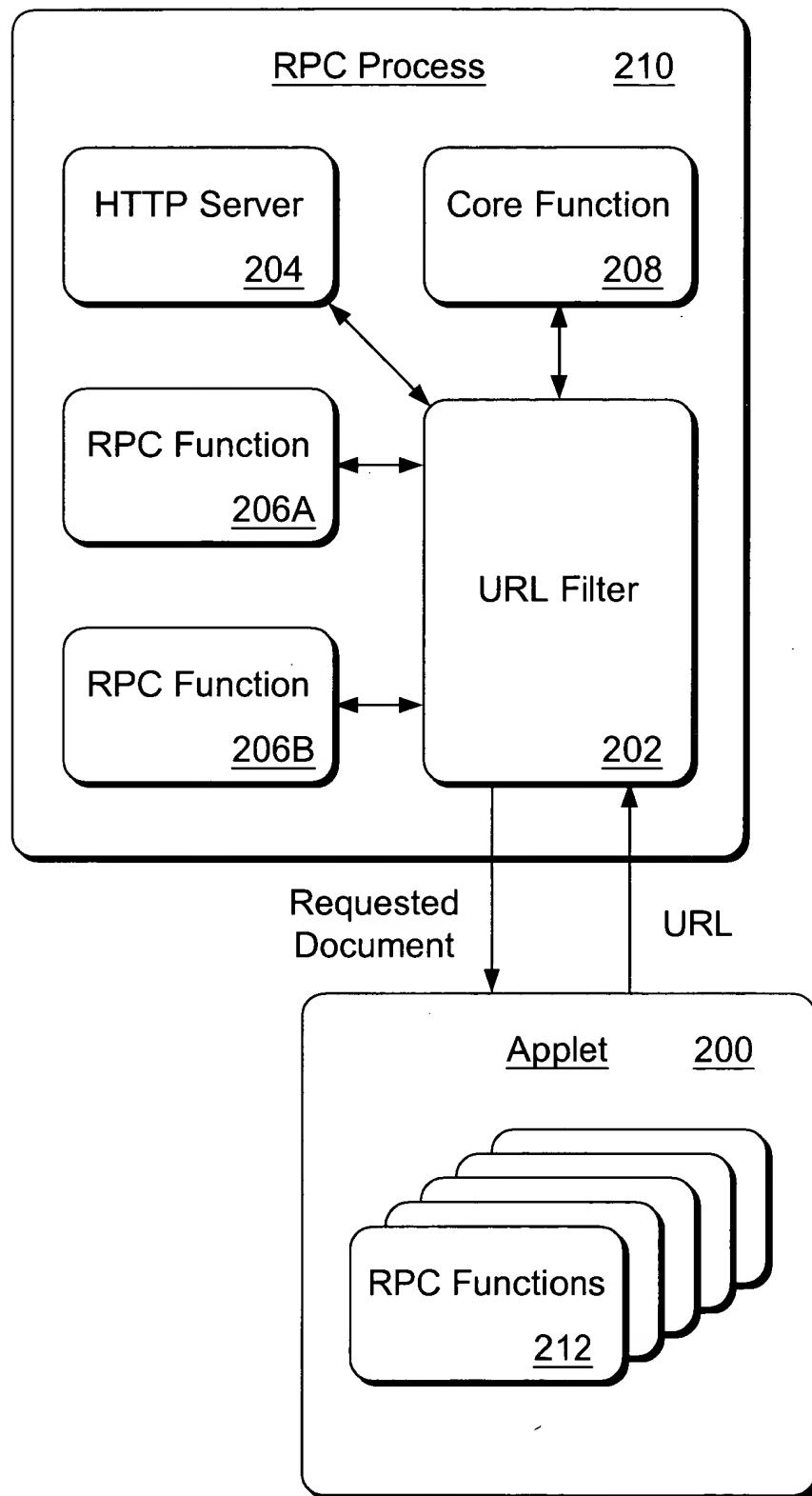


Fig. 2

APPROVED	O.G. FIG.
BY	
DAFTSMAN	

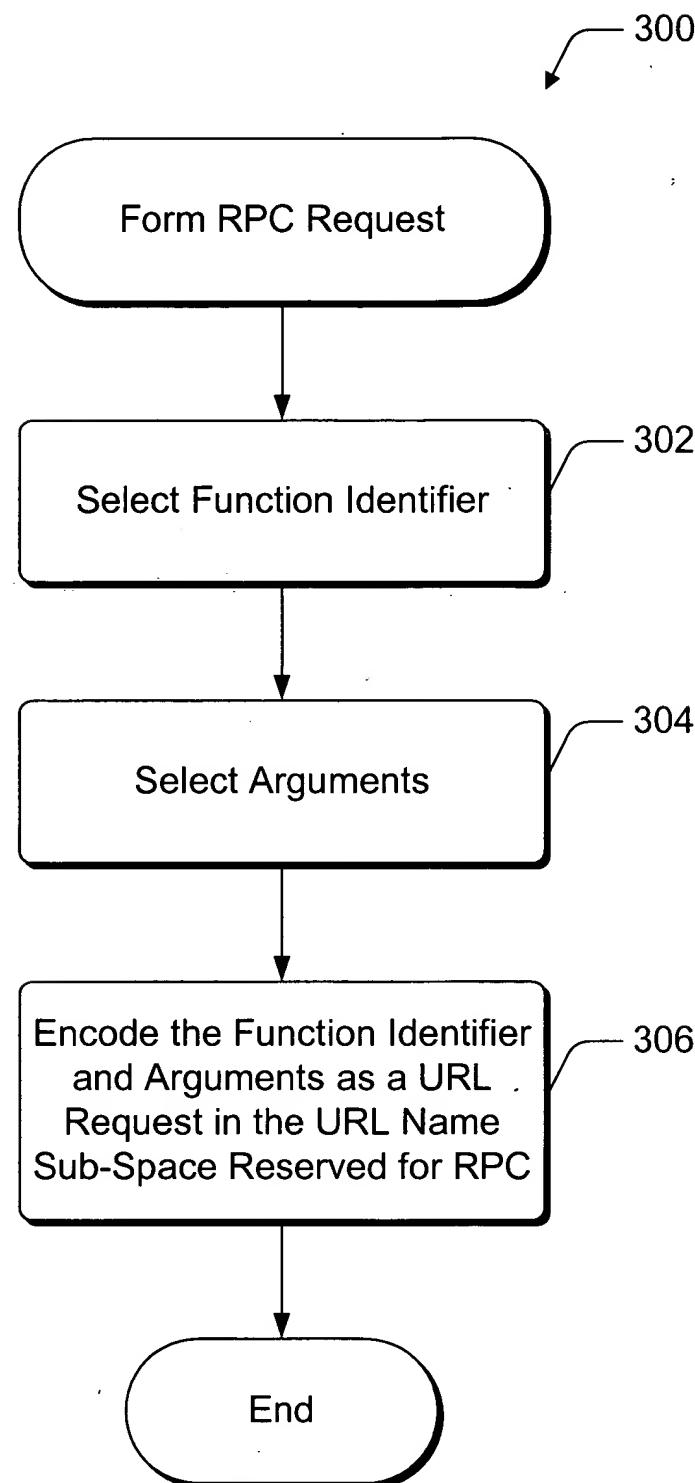


Fig. 3

APPROVED BY	O.G. FIG.
DRAFTSMAN	CLASS SUBCLASS

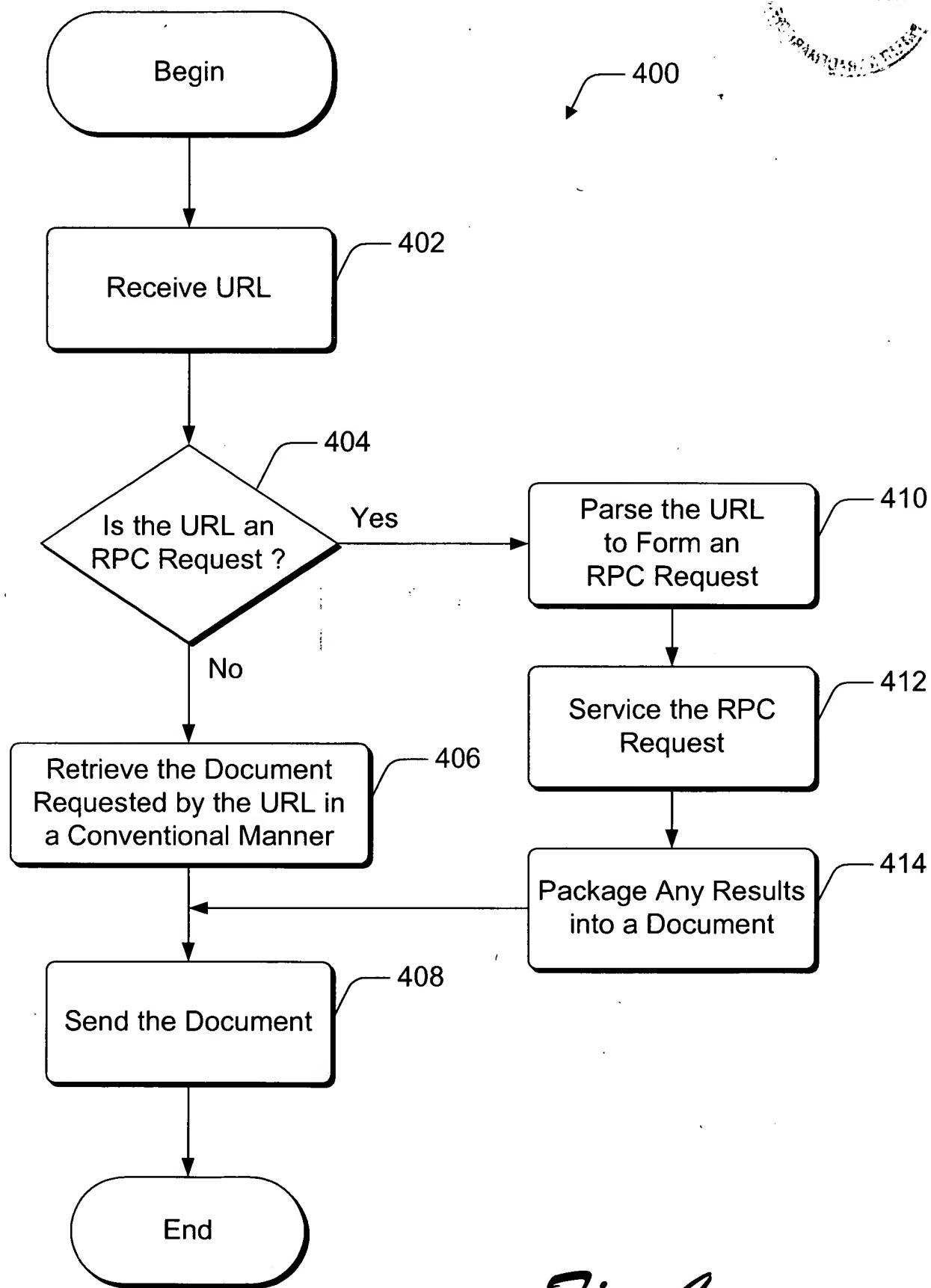


Fig. 4

APPROVED BY	O.G. FIG.
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MS1-796US 5 of 6

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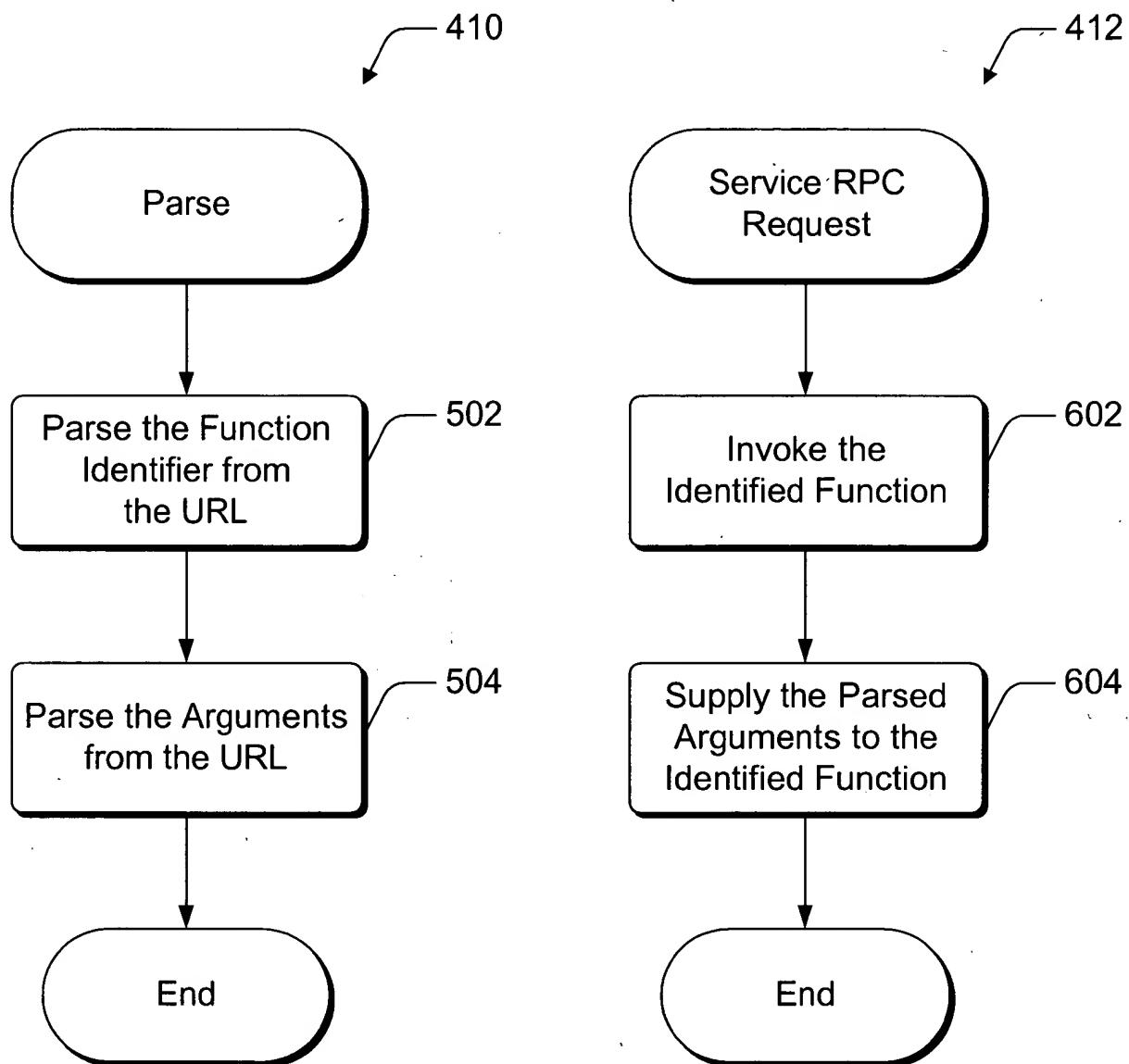


Fig. 5

Fig. 6

APPROVED BY	O.G. FIG.
DRAFTSMAN	CLASS SUBCLASS

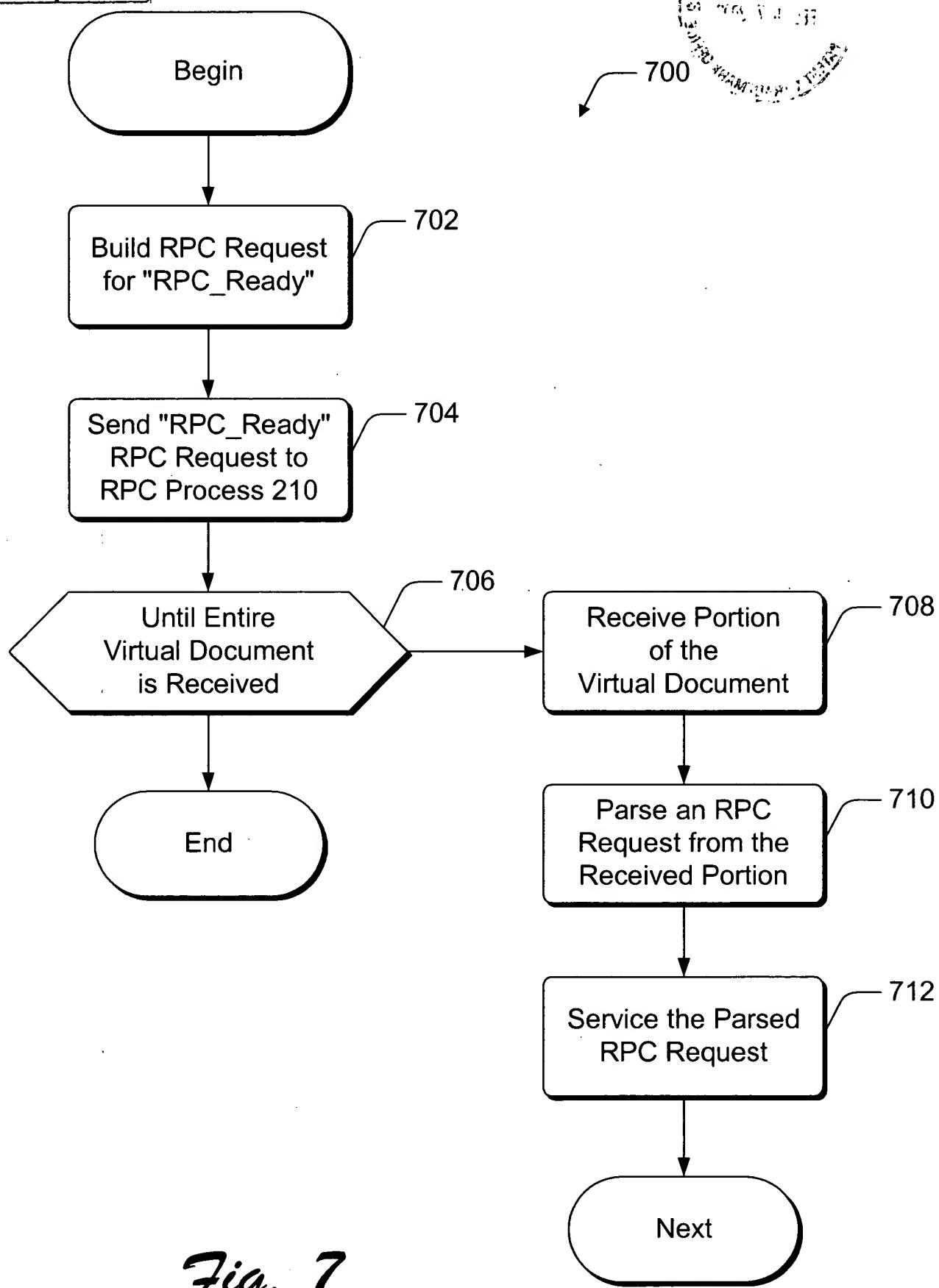


Fig. 7